

Department of Electrical Engineering
School of Electrical and Computer Sciences
IIT Bhubaneswar

**List of candidates shortlisted for technical interview round towards admission into PhD
Programme in Electrical Engineering Discipline for Spring 2025-26 (II) academic session**

Interview	Venue
Date of Interview: 06 th March 2026 (Friday) Reporting time for document verification: 9:00 am Interviews start after document verification from 10:00 am	School of Electrical and Computer Sciences IIT Bhubaneswar, Argul, Pin 752050

List of shortlisted candidates						
Sl. No.	Application no.	Name of Applicant	Gender	Category	School	Department
1	2025250010060	MANASI PATRO	female	GE	SECS	EE
2	2025250010124	RAJENDRA BABURAO GHARASE	male	OB	SECS	EE
3	2025250010132	HIMANSHU RAJ	male	SC	SECS	EE
4	2025250210152	RAKESH KUMAR	male	OB	SECS	EE
5	2025250310030	SUKANYA GHOSH	female	GE	SECS	EE
6	2025250410093	SARELLA BASAVAI AH	male	GE	SECS	EE
7	2025250510044	KRISHAN KANT KUMAR	male	OB	SECS	EE
8	2025250510053	SHEETAL KONDEL	female	EWS	SECS	EE
9	2025250510103	VIKAS DEV PANDEY	male	EWS	SECS	EE
10	2025250610071	KUMAR SHANU	male	SC	SECS	EE
11	2025250710078	SRINU INJETI	male	SC	SECS	EE
12	2025250710174	DHANRAJ DHARMARAJ DAPHALE	male	GE	SECS	EE
13	2025250910101	CHAUGULE SAGAR SHIVAJI	male	OB	SECS	EE