

Department of Electrical Engineering
School of Electrical and Computer Sciences
IIT Bhubaneswar

**List of candidates shortlisted for written test round towards admission into MS by
Research Programme in Electrical Engineering Discipline for Autumn 2025-26
academic session**

Reporting to Institute and document verification	Procedure of Exam	Venue
23 rd May 2025 (Friday) Reporting time: 9:00 am for document verification	Written Test: 23 rd May 2025 (Friday) from 9:30 am Interview: 23 rd May 2025 (Friday) for candidates qualifying the written exam	School of Electrical and Computer Sciences IIT Bhubaneswar, Argul, Pin 752050

List of shortlisted candidates			
#	Appl No	Name	Category
1	2025110010047	RISHABH PANDEY	GE
2	2025110010259	SWETA KUMARI	OB
3	2025110110312	AYUSHI JOLOTIA	GE
4	2025110410315	SHAIK HAMEEDA BANU	EWS
5	2025110510271	SOHAM CHAKRABORTY	GE
6	2025110610017	ARNAB SAMANTA	GE
7	2025110610146	AHMED TANISA	GE
8	2025110710192	DIVYASHA DASH	GE
9	2025110710196	DHARMVEER	GE
10	2025110810320	SOHOM GHOSH	GE